**PROGRAM OUTCOMES - (2018 REGULATIONS)**

**PO1:** An ability to independently carry out research investigation and development work to solve practical problems.

**PO2:** An ability to write and present a substantial technical report/document.

**PO3:** An ability to demonstrate a degree of mastery in the area of VLSI Design, with the mastery at a level higher than the requirements in the appropriate bachelor program.

**PO4:** An ability to apply knowledge of VLSI design concepts to solve engineering problems.

**PO5:** An ability to build competency and skills necessary to create VLSI system with the leading EDA tools and boards used in the semiconductor industry.

**PO6:** An ability to engage in life- long learning taking into consideration sustainability, societal, ethical and environmental aspects.